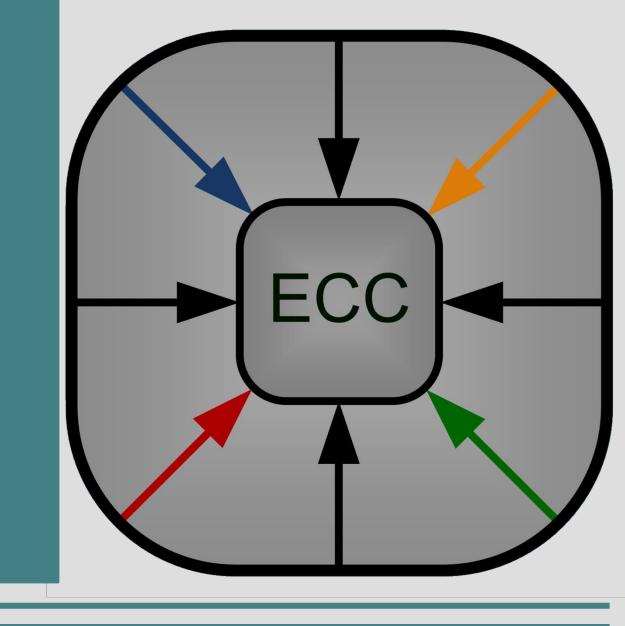


EMERGING CIRCUITS AND COMPUTATION

GROUP (ECC)

ISTANBUL TECHNICAL UNIVERSITY WWW.ECC.ITU.EDU.TR



Overview

- ECC group is operating in Computational Nanoelectronics Laboratory under the supervision of Dr. Mustafa Altun.
- Research areas of group are computing with switching nanoarrays, reliability of electronic boards, quantum circuits and stochastic computing and circuit design.
- Backgrounds of people are Electronics and Communication Engineering, Nanoscience and Nanoengineering, Control and Automation engineering etc.

Emerging Technologies
Computer Aided Design
Reliability
Nanoarrays
Stochastic Computing



Current Projects

- Implementation of a Defect-Aware 8-Bit Reversible Microprocessor.
- Synthesis and Performance Optimization of a Switching Nano-Crossbar Computer (NanoxComp)
- Synthesis and Reliability Analysis of Nano Switching Arrays

Research Areas

Reliability Analysis

- Developing less costly, yet accurate reliability analysis techniques.
- Using field return data, designing new accelerated tests.

Reliability Analysis Field return data Accelarated test Physics of failure based simulation Prediction Time

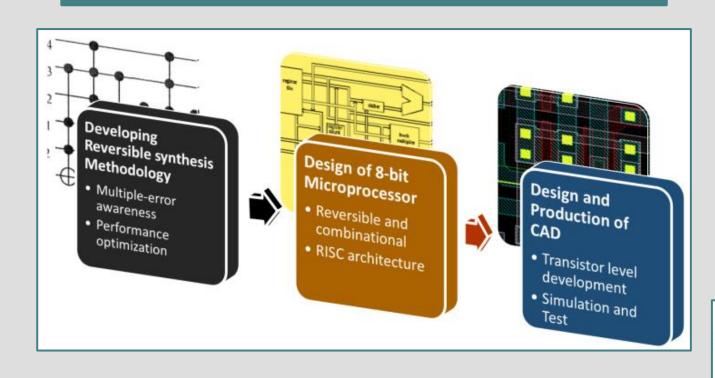
Stochastic Circuit Design

- Developing accurate arithmetic operation (addition and multiplication) with scalable stochastic circuit.
- Designing memory components
- Constructing a fully stochastic microprocessor.

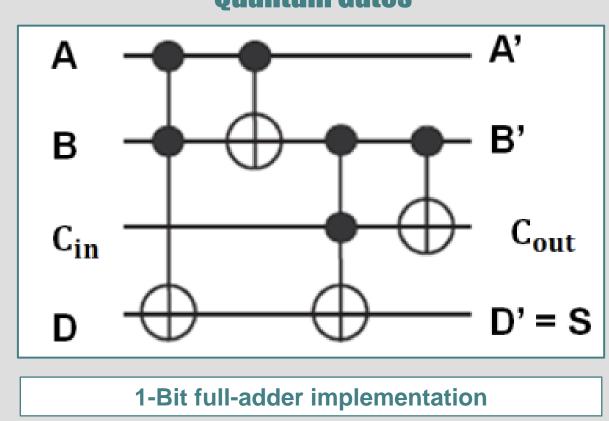
Design of Stochastic Arithmetic and Memory Component OUT ... 1010111000 Scalable 1010111000.... **Stochastic** ALU ... 0001011011 S1 S2 S3 ...1001 1001... Memory Component **♦** CLK Delay Accuracy Power Area **Production and Test of Stochastic ALU CMOS**

Reversible Computing

- Designing a reversible synthesis methodology with multiple error awareness
- Developing an 8-bit microprocessor with RISC architecture, reversible and combinational logic
- Design and production of CAD for reversible computing.

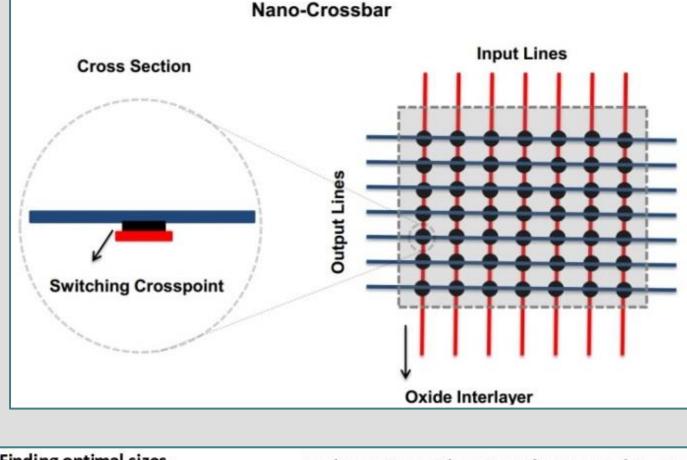


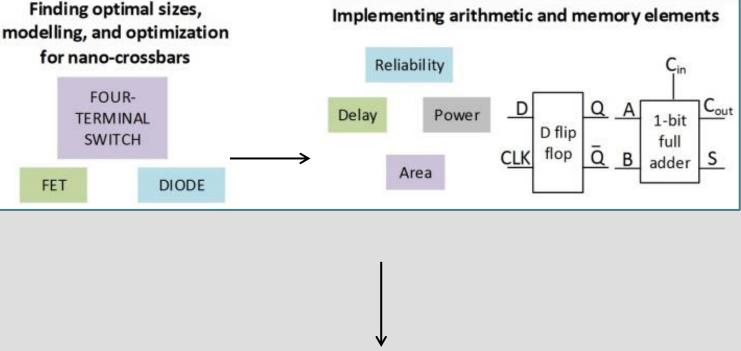
Classical Gates S Cin Cout Quantum Gates

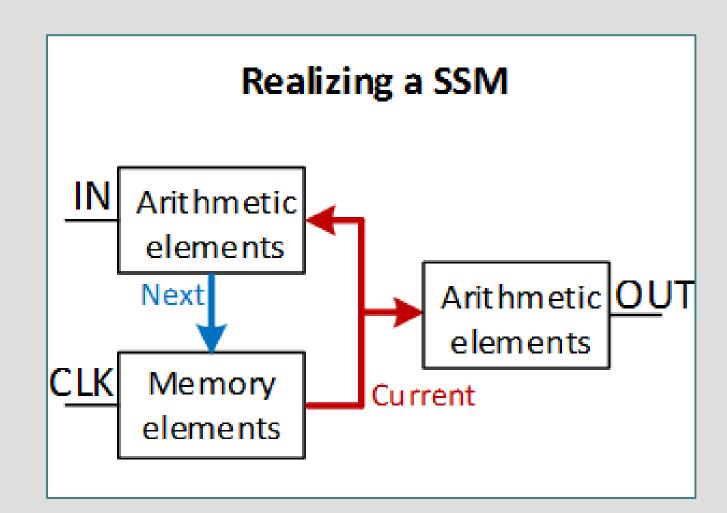


Computing with Switching Nanoarray

- Realizing an Synchronised State Machine (SSM) with switching nanoarrays
- Being a foundational methodolgy for future computing devices replacing CMOS.







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